



Appln. No. 10/043,763
Amended dated June 20, 2003
Reply to Office action of April 1, 2003

18/C
6/24/03
PATENT
Am H

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as first class mail in an envelope addressed to Commissioner of Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450 on June 20, 2003.

Christine Sherwood
Signature

Appln No. : 10/043,763
Applicant : Janardhanan S. Ajit
Filed : January 9, 2002
Title : SUB-MICRON HIGH INPUT VOLTAGE
TOLERANT INPUT OUTPUT (I/O)
CIRCUIT WHICH ACCOMMODATES
LARGE POWER SUPPLY VARIATIONS

TC/A.U. : 2816
Examiner : Terry D. Cunningham

Docket No. : 41980/PAN/B600

RECEIVED
JUN 25 2003
TECHNOLOGY CENTER 2800

AMENDMENT

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Post Office Box 7068
Pasadena, CA 91109-7068
June 20, 2003

Commissioner:

Pursuant to the request for continued examination (RCE) filed concurrently herewith and in response to the final rejection dated April 1, 2003, please amend the above identified application prior to examination as follows:

Amendments to the Claims are reflected in the list of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 5 of this paper.